IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLN. OF: UENO

FILED:

July 24, 2003

FOR:

SEMICONDUCTOR DEVICE MANUFACTURING APPARATUS AND

SEMICONDUCTOR DEVICE MANUFACTURING METHOD

DOCKET:

NEC 1030 DIV

MAIL STOP PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

The present application is a divisional of U.S. Patent Application Serial No. 09/273,627, filed March 23, 1999. Applicant hereby attaches copies of Form PTO-1449 and Forms PTO 892 from the parent application. Copies of the various prior art references listed in the attached Forms PTO-1449 and Forms PTO-892 are not being furnished, since they are available in the parent application. The claims in the present application are believed to be patentably distinguished over these references.

This information disclosure statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

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Docket: NEC 1030 DIV Information Disclosure Statement

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,

Norman P. Soloway Attorney for Applicant Reg. No. 24,315

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INFORMATION DISCLOSURE CITATION

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